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IBM Docket No. FIS920030233US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: **Thomas J. Fleischman**

Serial No.: **10/605,203** Date: **March 2, 2005**

Filed: **September 15, 2003** Group Art Unit: **2829**

Title: **APPARATUS AND METHOD FOR  
AUTOMATIC ELIMINATION OF  
ROUND-TRIP DELAY ERRORS  
INDUCED BY AUTOMATIC TEST  
EQUIPMENT CALIBRATION** Examiner: **Ernest F. Karlsen**

**DeLIO & PETERSON, LLC**  
121 Whitney Avenue  
New Haven, CT 06510-1241

**LETTER TO OFFICIAL DRAFTSMAN**

**Mail Stop ISSUE FEE**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

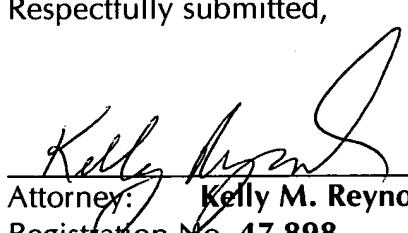
Dear Sir:

Applicants are herewith submitting 3 sheets of Formal Drawings for the approval of the Draftsman.

Please charge the Assignee IBM Corporation Deposit Account No. **09-0458** for any fee related to the acceptance of the attached Formal Drawings. A duplicate copy of this letter is enclosed for that purpose.

Respectfully submitted,

BY:

  
Attorney: **Kelly M. Reynolds**  
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